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PATENT
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Applicant(s): Philip D. Steiner et al.

Title: METHOD AND APPARATUS FOR DETERMINING A LOSS OF SIGNAL CONDITION

Application No.: 10/015,030

Filed: December 11, 2001

Examiner: Unknown

Group Art Unit: 2133

Atty. Docket No.: 026-0013

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- ☒ Form(s) PTO-1449
☐ Other: n/a

to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

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For each item of information listed that is not in the English language, the undersigned has provided a concise explanation of the relevance through (i) an English language abstract, (ii) an English language equivalent application, or (iii) if cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action that indicates the degree of relevance found by the foreign office.

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01 FC:126 180.00 CH

Adjustment date: 04/16/2003 LMARKS
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FEE AUTHORIZATION

- ☐ This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
- ☒ The undersigned believes that this Information Disclosure Statement is being filed before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.
- ☒ If however, this Information Disclosure Statement is filed after the period specified in § 1.97(b), the undersigned hereby authorizes the Commissioner to charge the fee set forth in § 1.17(p) to Deposit Account No. 50-0631.

CERTIFICATE OF MAILING

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Mark Zagorin

4/4/02
Date

Respectfully submitted,



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U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 026-0013		
				Application No.: 10/015,030		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s): Philip D. Steiner et al.		
(Use several sheets if necessary)				Filing Date: December 11, 2001		
				Group Art Unit: 2133		
				Date Submitted: April 4, 2002		
U.S. Patent Documents						
*Examiner Initial		Document Number	Date	Name		
	AA			RECEIVED MAY 02 2002 Technology Center 2600		
	AB					
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	AG					
	AH					
	AI					
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	AK					
Foreign Patent Documents						
				Translation		
		Document	Date	Country	Yes	No
	AL					
	AM					
	AN					
	AP					
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
	AR	Giga, "2.5 Gbit/s Clock and Data Recovery GD16522", Data Sheet Rev. 20, Giga, September 25, 2000, pp. 1-11				
	AS	Maxim, "2.5Gbps, Low-Power, +3.3V Clock Recovery and Data Retiming IC", Maxim Integrated Products, 1998, pp. 1-8				
	AT					
Examiner			Date Considered			
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